stinere am F monster inside your apple?

fG! @ CODE BLUE 2015

Who am I?

- An Economist.
- Who loves Human Behavior.
- And politics.
- Oh, and a bit of computers.









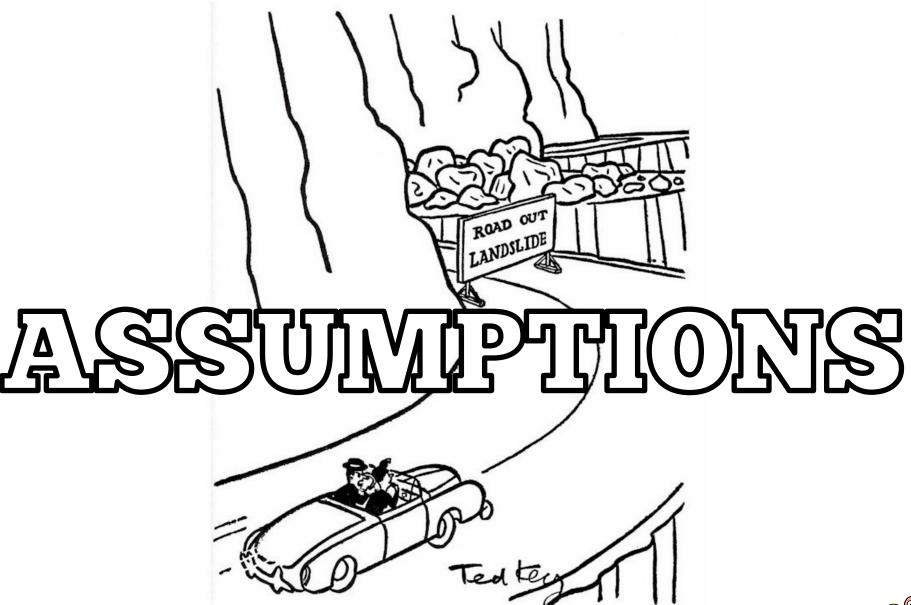
Whats UP Doc?



EFI Monsters?

- Introduction to EFI.
- How to
 - Reverse engineer (U)EFI binaries.
 - Search for (U)EFI rootkits.





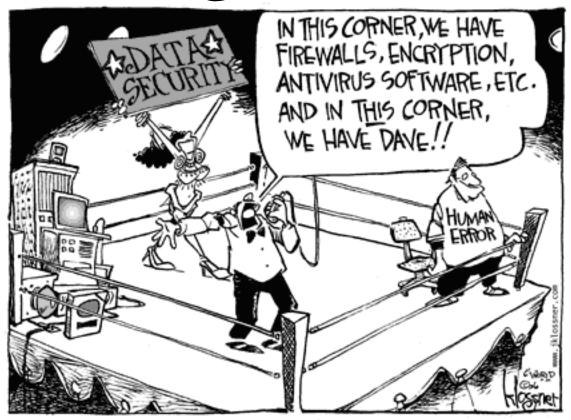
"Relax! I know this road perfectly! I've been driving it all my life!"



Assumptions

- Reference machine
 - MacBook Pro Retina 10.1.
- 64-bit only OS X versions.
- Sandy Bridge or newer.







Why EFI?

- BIOS replacement.
- Initially developed by Intel.
 - http://www.intel.com/content/www/us/en/ architecture-and-technology/unified-extensiblefirmware-interface/efi-specifications-generaltechnology.html
- Now UEFI, managed by UEFI consortium.
 - http://www.uefi.org



Why EFI?

- Initializes your machine.
- Access to low level features.
- Modular.
- Feature rich.
- Rather easy development in C.







- Diskless kernel/userland rootkits
- Rootkit data stored in the flash chip.
- Unpack and patch kernel on boot.
- RAM only, never touch hard-disk.
- Check Snare's SyScan 2012 presentation.



- Can be hard to detect.
- With regular available tools.
- And with some anti-forensics.
- For example anti-memory dumping.



- Persistence across operating system installs
- HackingTeam built a UEFI rootkit.
 - https://github.com/hackedteam/vector-edk
 - https://github.com/informationextraction/vectoredk/blob/master/MdeModulePkg/Application/ fsbg/fsbg.c



- Attack full-disk encryption
- Install a keylogger.
- Recover FileVault2 password.

```
Loading kernel cache file 'System\Library\Caches\
ernelcache'...

root device uuid is '7A18BC97-4624-3FE9-A158-41D2|
+++++ ExitBootServices +++++
***** Password: '2pwtwo!\x000D'
Starting OS... 10 OF OE 00 OC OB OA 09 08 07 06 05
```



- Attack "secure" operating systems
- For example, Tails.
- Recover PGP keys and/or passphrases.
- https://www.youtube.com/watch?v=sNYsfUNegEA.



- Bootloader
 - Redirect to a custom bootloader.
- SMM backdoors
 - http://blog.cr4.sh/2015/07/building-reliablesmm-backdoor-for-uefi.html







there was a...









Mac attack! Nasty bug lets hackers into Apple computers











By Jose Pagliery @Jose_Pagliery



Mac bug makes rootkit injection as easy as falling asleep

Apple hacker reveals cracker 0day rootkit whacker

Security

Related topics

Apple, Security



- Firmware related zero day.
- Disclosed a few months ago.
 - https://reverse.put.as/2015/05/29/theempire-strikes-back-apple-how-your-macfirmware-security-is-completely-broken/



- Failure to lock the flash.
- Write to the flash from userland.
- Similar to Thunderstrike but better.
- Thunderstrike requires physical access.
- Prince Harming allows remote attack.



PERSISTENCE FIRMWARE FLASH

- ▶ Hardware-specific, but it's always there
- Can modify everything
 - SEC, PEI, DXE, BDS, custom drivers, whatever
- ▶ Can be written to from the OS
- ▶ So awesome. | | / | 0 A++++ would buy again.



- Extremely simple to trigger.
- Put machine to sleep.
 - Close, wait for fans to stop, and reopen.
 - Or force sleep with "pmset sleepnow".



- Sandy Bridge and Ivy Bridge Macs are vulnerable.
- Haswell or newer are not.
- All older machines are vulnerable
 - Core 2 Duo or older.
 - No flash protections at all.



Available updates:

MacBook A	ir MacBook F	Pro Mac Mini	Mac Pro	iMac
4,1	8,1	5,1	6,1	12,1
5,1	9,1	6,1		13,1
6,1	10,1	7,1		14,1
7,1	10,2			14,2
	11,1			14,3
	11,2			14,4
	11,4			15,1
	12,1			

- Reversing and understanding the vulnerability.
 - https://reverse.put.as/2015/07/01/reversingprince-harmings-kiss-of-death/
- Contains links to relevant EFI documentation.



- Venamis aka Dark Jedi was also patched.
 - http://events.ccc.de/congress/2014/Fahrplan/ events/6129.html
 - http://blog.cr4.sh/2015/02/exploiting-uefiboot-script-table.html
- Slightly more complex, same results.



- The story doesn't end here.
- Check ThunderStrike 2 slides.
- Other unpatched vulnerabilities.
- Can be exploited with remote attack vectors.



Old bugs, new platforms

Vulnerability	Private disclosure Public disclosure	Status on OSX	
Snorlax/PrinceHarming VU #577140	August 2013 July 2015 / May 2015	Patched June 2015	
Darth Venamis VU #976132	Sept 2014 Dec 2014	Partial Patch June 2015	
SpeedRacer/BIOS_CTNL VU #766164	Dec 2013 Aug 2014	Vulnerable	
King's Gambit VU #552286	Dec 2013 Aug 2014	Vulnerable (See HITB-GSEC 2015)	
The Sicilian VU #255726	~May 2013 Sep 2013	Vulnerable	
Setup UEFI Variable VU #758382	June 2013 Mar 2014	Not vulnerable	



Reminder: This talk has 1 main point

 Apple has not been as responsive, or as accurate, as other PC vendors in responding to industry-wide notifications of firmware vulnerabilities. Consequently Mac users have been left vulnerable to attacks that have been fixed on other x86-based PCs.

Apple ...







Where is EFI?

- Usually stored in a CMOS serial flash.
- Two popular chips
 - Macronix MX25L6406E.
 - Micron N25Q064A.
- SPI compatible.
- Most are 64 Mbits/8 Mbytes.





- Newer machines flash chip(s)
 - Winbond W25Q64FV.
- Chip list from EfiFlasher.efi:

SST 25VF080	Macronix 25L1605	ST Micro M25P16	WinBond 25X32
SST 25VF016	Macronix 25L3205	ST Micro M25P32	Winbond 25X64
SST 25VF032	Macronix 25L6436E	Eon M25P32	Winbond 25X128
SST 25VF064	Atmel 45DB321	Eon M25P16	Numonyx N25Q064



- Most chips are 8 pin SOIC.
- SMD or BGA versions used?
 - Retinas 13"?
 - New MacBook 12"?



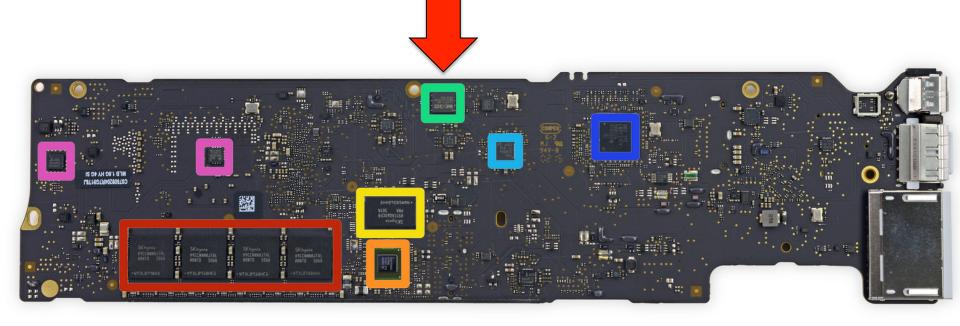
- You can buy the chips bulk and cheap.
- Useful for flashing experiments.
- Good results from Aliexpress.com.
- Around \$14 for 10 N25Q064A.
- Around \$8 for 10 MX25L640E.



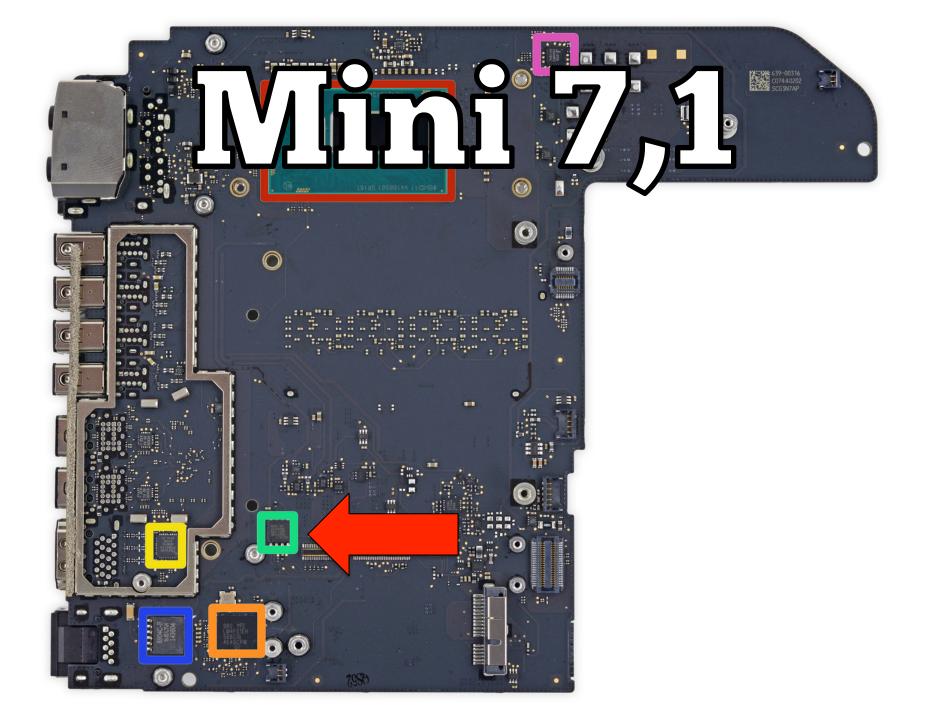
- Easy access on some models.
 - Retinas 15" are the easiest.
- Extensive disassembly required on others.
- Still, a MacBook Pro 8,1 can be disassembled in 5 mins or less.

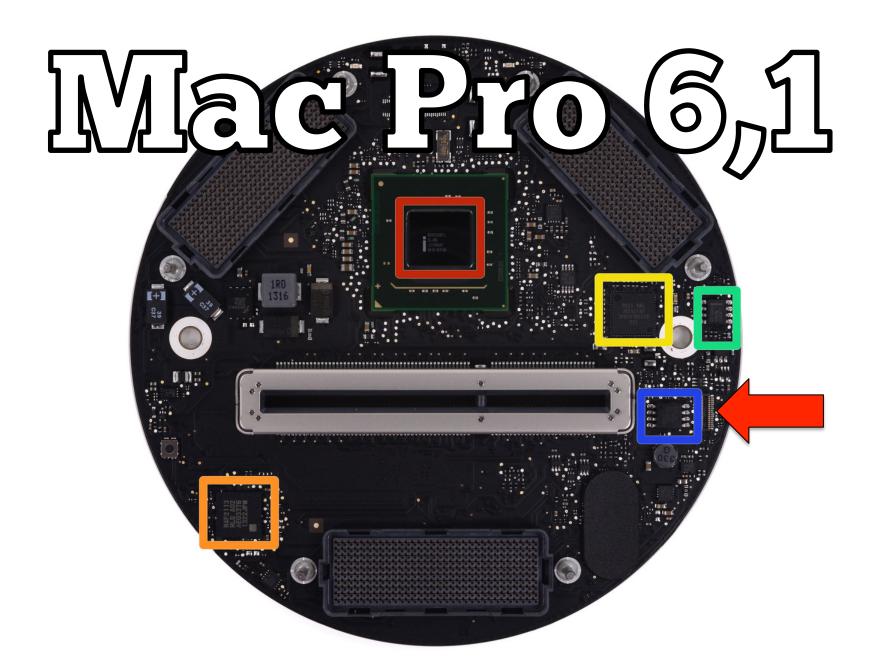


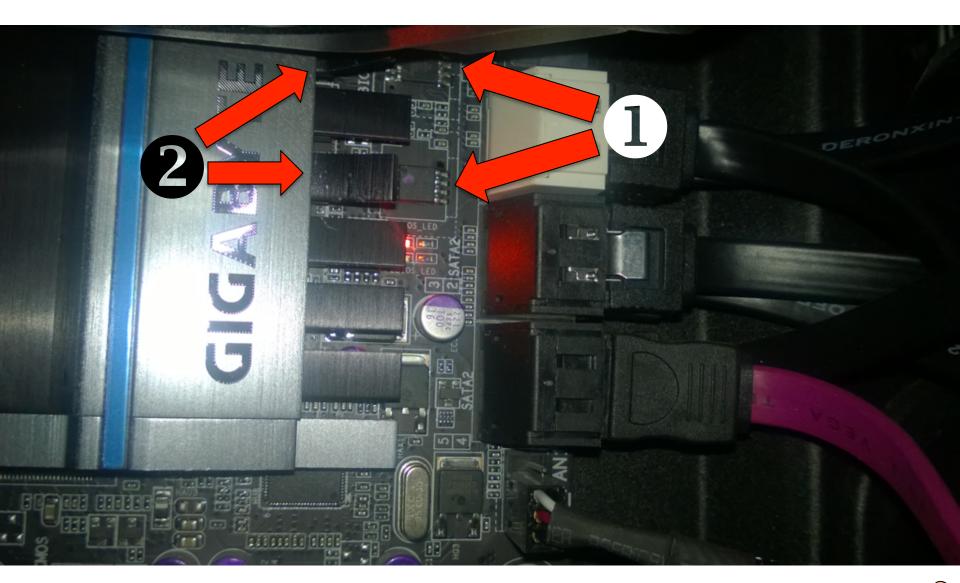




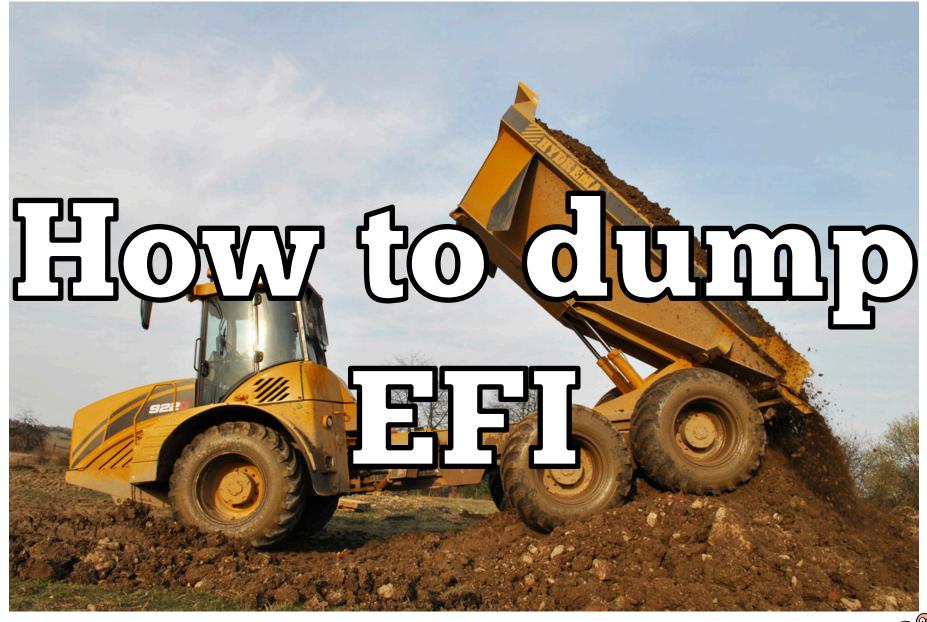














How to dump EFI

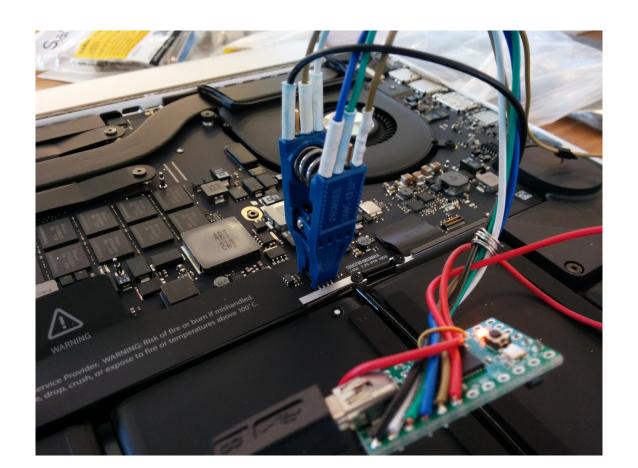
- Hardware
 - The best and most reliable way.
 - Trustable.
- Software
 - Possible if chip supported by flashrom.
 - Not (very) trustable.



- Any SPI compatible programmer.
 - http://flashrom.org/Supported_programmers
- I use Trammell Hudson's SPI flasher.
 - https://trmm.net/SPI



Based on Teensy 2.0 or 3.x.

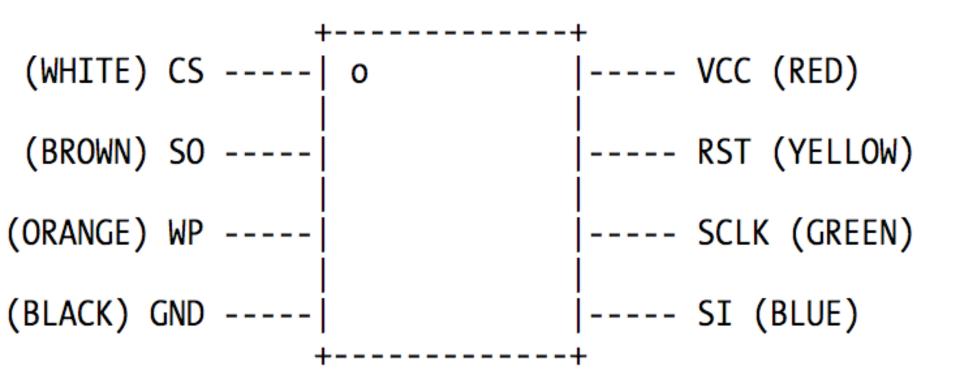




- Easy to build.
- Cheap, ~ \$30.
- Fast, dumps a 64Mbit flash in 8 mins.
- The Teensy 3 version is even faster.
- It just works!

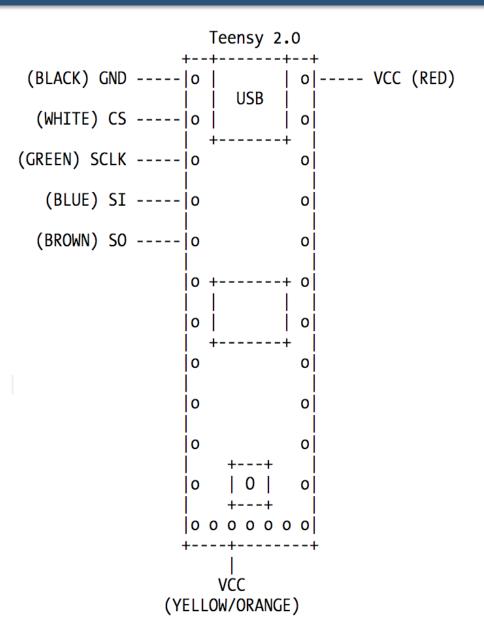


Flash chip SPI pinout





Teensy 2.0 pinout



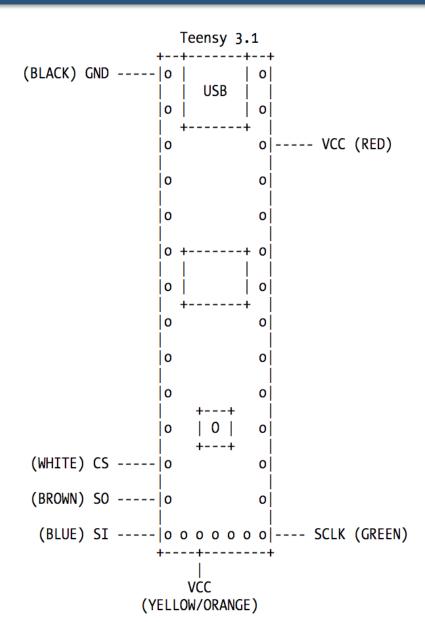


Teensy 2.0 pinout

- Teensy 2 default voltage is 5v.
- Flash chips are 3.3.v.
- Requires voltage regulator MCP1825.
- https://www.pjrc.com/store/mcp1825.html



Teensy 3.1 pinout





Tips & Tricks

- Shunt WP and RST pins to VCC.
- Different SPI pins names
 - SCLK, SCK, CLK.
 - MOSI, SIMO, SDO, DO, DOUT, SO, MTSR.
 - MISO, SOMI, SDI, DI, DIN, SI, MRST.
 - SS, nCS, CS, CSB, CSN, nSS, STE, SYNC.



How to read entire flash

```
lrx: ready to receive Retina-09-07-2015-Secuinside.bin
^Clrx: caught signal 2; exiting

real  6m58.773s
user  0m0.774s
sys  0m1.726s

$ ls -la Retina-09-07-2015-Secuinside.bin
```

\$ time lrx -X -0 </dev/cu.usbmodem12341 >/dev/cu.usbmodem12341 Retina-09-07-2015-Secuinside.bin

1 reverser staff 8388608 Jul 9 16:47 Retina-09-07-2015-Secuinside.bin



How to write entire 64MB flash

```
spi
>Help:
i: print ID
r: read 16 bytes from address - r0<enter>
R: read XX bytes from address - RO 10<enter>
d: dump to console
w: write enable interactive
e: erase sector interactive
u: upload
b: upload bios area only
1: flash first ffs
2: flash second ffs
3: flash third ffs
x: download
u
>0 800000
(exit to shell)
# pv new-efi.bin > /dev/cu.usbmodem12341
```



- Linux works best to write the flash.
- Some issues with OS X version.
- pv or serial driver issues?
 - http://www.ivarch.com/programs/pv.shtml



- Requirements
 - Flashrom
 - DirectHW.kext
- Rwmem by Trammell also works.
- Or readphysmem.



- DarwinDumper.
- Contains binary versions of flashrom and DirectHW.kext.
- Kernel extension is not code signed.
- (Still) Whitelisted by Apple.



- http://flashrom.org/Flashrom
- http://www.coreboot.org/DirectHW
- https://bitbucket.org/blackosx/ darwindumper/downloads
- https://github.com/osresearch/rwmem
- https://github.com/gdbinit/readphysmem



```
sh-3.2# kextload DirectHW.kext/

sh-3.2# ./flashrom -r bios_dump.bin -V -p internal
flashrom v0.9.7-r1711 on Darwin 14.4.0 (x86_64)
flashrom is free software, get the source code at http://www.flashrom.org

flashrom was built with libpci 3.1.7, LLVM Clang 6.0 (clang-600.0.56), little endian
Command line (5 args): ./flashrom -r bios_dump.bin -V -p internal
(...)
Found chipset "Intel HM77" with PCI ID 8086:1e57.
This chipset is marked as untested. If you are using an up-to-date version
of flashrom *and* were (not) able to successfully update your firmware with it,
then please email a report to flashrom@flashrom.org including a verbose (-V) log.
Thank you!
```



```
SPI Read Configuration: prefetching disabled, caching enabled, OK.
The following protocols are supported: FWH, SPI.
(..)
Probing for Micron/Numonyx/ST N25Q064..3E, 8192 kB: probe_spi_rdid_generic: id1 0x20, id2 0xba17
Found Micron/Numonyx/ST flash chip "N25Q064..3E" (8192 kB, SPI) at physical address 0xff800000.
Chip status register is 0x00.
Chip status register: Status Register Write Disable (SRWD, SRP, ...) is not set
Chip status register: Block Protect 3 (BP3) is not set
Chip status register: Top/Bottom (TB) is top
Chip status register: Block Protect 2 (BP2) is not set
Chip status register: Block Protect 1 (BP1) is not set
Chip status register: Block Protect 0 (BP0) is not set
Chip status register: Write Enable Latch (WEL) is not set
Chip status register: Write In Progress (WIP/BUSY) is not set
(...)
```



```
Found Micron/Numonyx/ST flash chip "N25Q064..3E" (8192 kB, SPI). This chip may contain one-time programmable memory. flashrom cannot read and may never be able to write it, hence it may not be able to completely clone the contents of this chip (see man page for details). Reading flash... done.

Restoring MMIO space at 0x10ae098a0
```

sh-3.2# ls -la bios_dump.bin -rw-r--r- 1 root staff 8388608 Jul 8 01:23 bios_dump.bin

Restoring PCI config space for 00:1f:0 reg 0xdc



- AppleHWAccess.kext.
- readphysmem utility.
- Can read bios without external kext.
- Default on Mavericks and Yosemite.
- Not anymore on El Capitan.

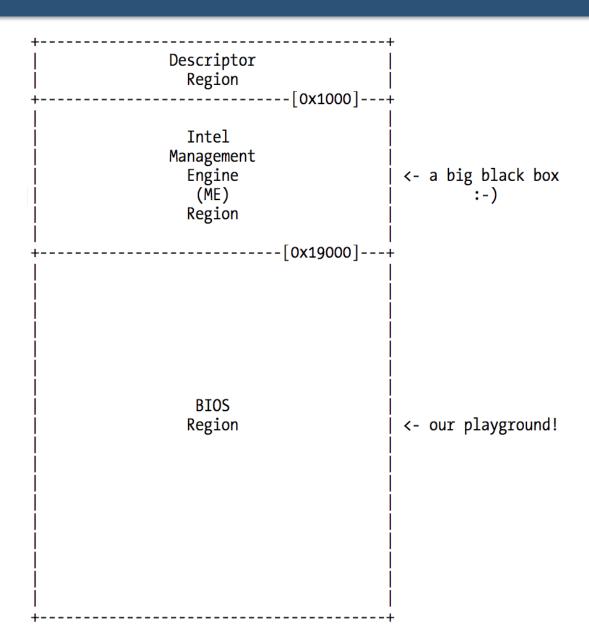


- Good enough to play around.
- Mostly useless to chase (U)EFI rootkits.
- Unless it is made by HackingTeam.
 - Their version makes no attempt to hide itself from software dumps.

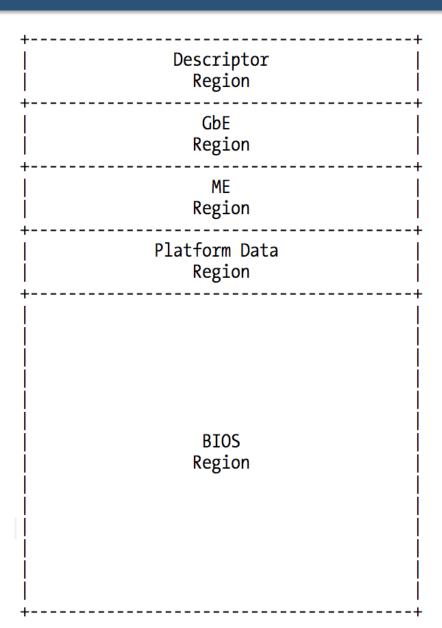


What is im the flash?











				Information
Name	Action Type	Subtype	Text	Full size: 1000h (4096)
▼ Intel image	Image	Intel		ME region offset: 1000h
Descriptor region	Region	Descriptor		BIOS region offset: 190000h Region access settings:
ME/TXE region	Region	ME/TXE		BIOS:FFFFh ME:FFFFh GbE:FFFF
▼ BIOS region	Region	BIOS		BIOS access table:
▶ 7A9354D9-0468-444A-81CE-0BF617D890DF	Volume	FFSv2	AppleCRC32 AppleFS0	Read Write
▶ 7A9354D9-0468-444A-81CE-0BF617D890DF	Volume	FFSv2	AppleCRC32 AppleFS0	Desc Yes Yes
▶ 7A9354D9-0468-444A-81CE-0BF617D890DF	Volume	FFSv2	AppleCRC32 AppleFS0	BIOS Yes Yes
E3B980A9-5FE3-48E5-9B92-2798385A9027	Volume	Unknown	AppleCRC32 AppleFS0	ME Yes Yes
▶ 7A9354D9-0468-444A-81CE-0BF617D890DF	Volume	FFSv2	AppleCRC32 AppleFS0	GbE Yes Yes
▶ 7A9354D9-0468-444A-81CE-0BF617D890DF	Volume	FFSv2	AppleCRC32 AppleFS0	Flash chips in VSCC table:
153D2197-29BD-44DC-AC59-887F70E41A6B	Volume	Unknown	AppleCRC32	1F4700h
153D2197-29BD-44DC-AC59-887F70E41A6B	Volume	Unknown	AppleCRC32	EF4017h
FFF12B8D-7696-4C8B-A985-2747075B4F50	Volume	Unknown		C22017h
▶ 7A9354D9-0468-444A-81CE-0BF617D890DF	Volume	FFSv2	AppleCRC32 AppleFS0	BF254Bh
▶ 7A9354D9-0468-444A-81CE-0BF617D890DF	Volume	FFSv2	AppleCRC32 AppleFS0	20BA17h
▶ 04ADEEAD-61FF-4D31-B6BA-64F8BF901F5A	Volume	FFSv2	AppleCRC32 AppleFS0	
▶ 04ADEEAD-61FF-4D31-B6BA-64F8BF901F5A	Volume	FFSv2	AppleFS0	

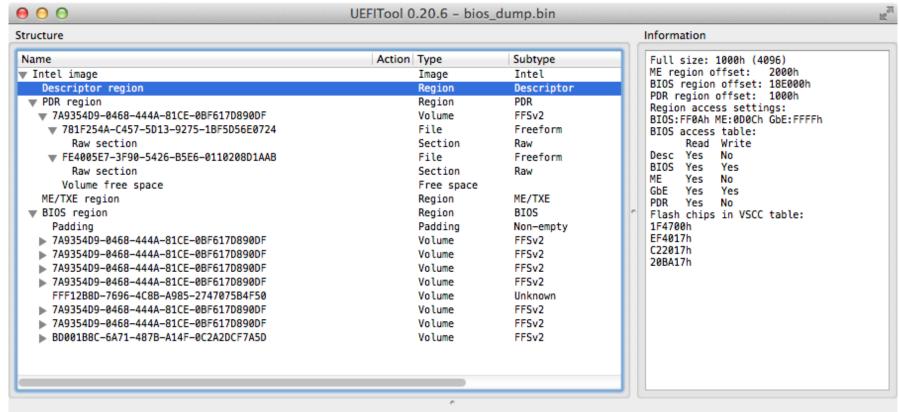
UEFITool 0.20.6 - Retina-08-07-2015-after-SyScan-dump-and-EFI-update-09.bin

Opened: Retina-08-07-2015-after-SyScan-dump-and-EFI-update-09.bin

parseVolume: unknown file system E3B980A9-5FE3-48E5-9B92-2798385A9027 parseVolume: unknown file system 153D2197-29BD-44DC-AC59-887F70E41A6B parseVolume: unknown file system 153D2197-29BD-44DC-AC59-887F70E41A6B parseVolume: unknown file system FFF12B8D-7696-4C8B-A985-2747075B4F50

Messages





Messages

parseVolume: unknown file system FFF12B8D-7696-4C8B-A985-2747075B4F50

parseVolume: non-UEFI data found in volume's free space

Opened: bios_dump.bin



Descriptor region

- Location of other regions.
- Access permissions.
 - OS/BIOS shouldn't access ME region.
- VSCC configures ME flash access.



Intel ME region

- A CPU inside your CPU ②.
- Runs Java.
- Can be active with system powered off.
- Out of band network access!
- No access from BIOS and OS.



Intel ME region

- Mostly a blackbox.
- Three presentations by Igor Skochinsky.
- Definitely requires more research!
- Unpacker
 - http://io.smashthestack.org/me/



Intel ME region

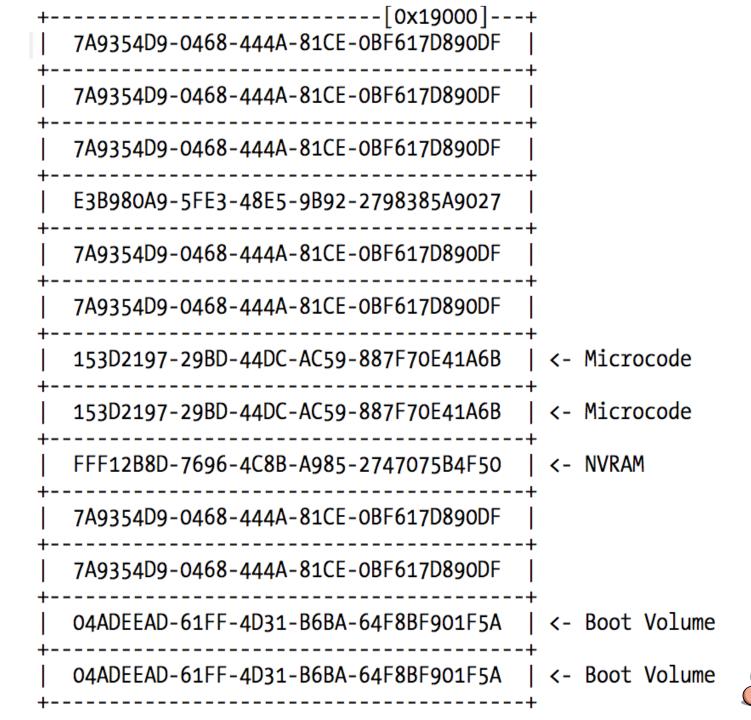
- Rootkit in your laptop: Hidden code in your chipset and how to discover what exactly it does
- Intel ME Secrets
- Intel ME: Two years later
- https://github.com/skochinsky/papers



BIOS region

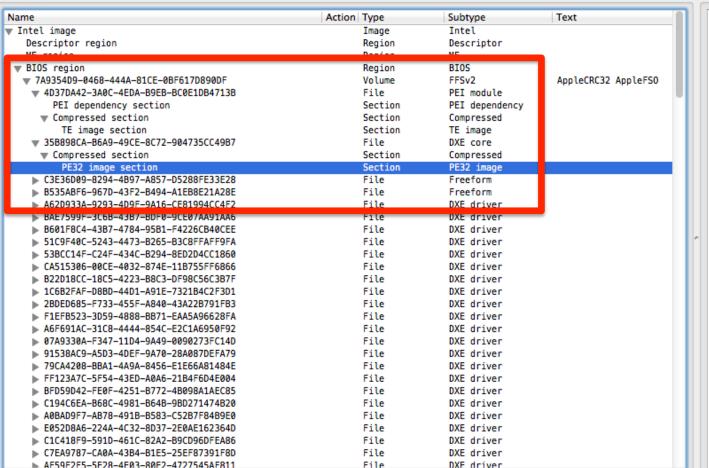
- Contains
 - EFI binaries for different phases.
 - NVRAM.
 - Microcode (not for some models).
- Each on its own firmware volume (FVH).











Information

Type: 10h

Full size: 1A388h (107400) Header size: 4h (4) Body size: 1A384h (107396) DOS signature: 5A4Dh PE signature: 00004550h Machine type: x86-64 Number of sections: 4 Characteristics: 030Eh

Optional header signature: 020Bh

Subsystem: 000Bh

RelativeEntryPoint: 6B9Fh

BaseOfCode: 240h ImageBase: 0h EntryPoint: 6B9Fh

Messages

parseVolume: unknown file system E3B980A9-5FE3-48E5-9B92-2798385A9027 parseVolume: unknown file system 153D2197-29BD-44DC-AC59-887F70E41A6B parseVolume: unknown file system 153D2197-29BD-44DC-AC59-887F70E41A6B parseVolume: unknown file system FFF12B8D-7696-4C8B-A985-2747075B4F50

Opened: Retina-30-07-2015-after-Secuinside-2015.bin



BIOS region

- Everything is labeled with a GUID.
- No filenames.
- Many GUID can be found in EFI specs.
- Others are vendor specific/private.
- Google and luck are your friends!



Branch: master - ida-efiutils / efiguids_ami.py





snare on May 28, 2013 Anon contribution of GUIDs

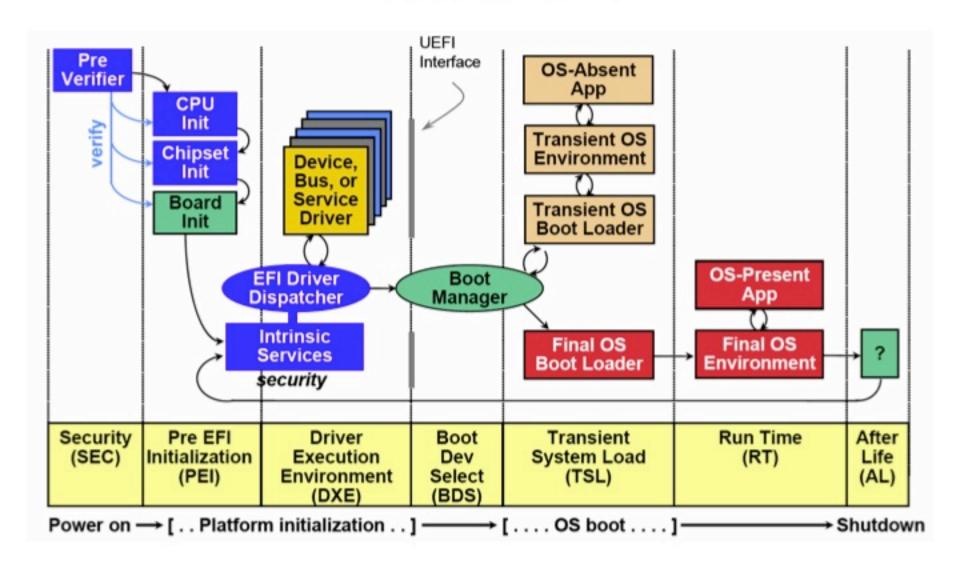
1 contributor

```
911 lines (906 sloc) 96.802 kB
                                                                                         Raw
                                                                                                Blame
                                                                                                        History
       .....
      efiguids_ami.py
   2
   3
   4
       GUIDs found in the AMI source
   5
       See the following URL for more info and the latest version:
   6
       https://github.com/snarez/ida-efiutils
   7
   8
       .....
   9
  10
       GUIDs = {
  11
       'ACOUSTIC SETUP PROTOCOL GUID': [0xc1d7859d, 0x5719, 0x46c3, 0xa2, 0x98, 0xd0, 0x71, 0xe3, 0x2, 0x64, 0xd1],
  12
       'ADD_BOOT_OPTION_GUID':[0x19d96d3f, 0x6a6a, 0x47d2, 0xb1, 0x95, 0x7b, 0x24, 0x32, 0xda, 0x3b, 0xe2],
  13
       'ADVANCED_FORM_SET_GUID':[0xe14f04fa, 0x8706, 0x4353, 0x92, 0xf2, 0x9c, 0x24, 0x24, 0x74, 0x6f, 0x9f],
  14
       'AHCI_BUS_INIT_PROTOCOL_GUID':[0xB2FA4764, 0x3B6E, 0x43D3, 0x91, 0xDF, 0x87, 0xD1, 0x5A, 0x3E, 0x56, 0x68],
  15
       'AHCI SMM_PROTOCOL GUID':[0xB2FA5764, 0x3B6E, 0x43D3, 0x91, 0xDF, 0x87, 0xD1, 0x5A, 0x3E, 0x56, 0x68],
  16
       'AMICSM_PCIBUSNUM_XLAT_PROTOCOL_GUID':[0xcb5c54c0, 0x230d, 0x43db, 0x92, 0x2c, 0x24, 0xd3, 0x4f, 0x8c, 0x91, 0x5c],
  17
       'AMITSESETUP GUID': [0xc811fa38, 0x42c8, 0x4579, 0xa9, 0xbb, 0x60, 0xe9, 0x4e, 0xdd, 0xfb, 0x34],
  18
       'AMITSE_ADMIN_PASSWORD_VALID_GUID':[0x541d5a75, 0x95ee, 0x43c7, 0x9e, 0x5d, 0x23, 0x94, 0xdc, 0x48, 0x62, 0x49],
  19
  20
       'AMITSE_AFTER_FIRST_BOOT_OPTION_GUID':[0xC48D651C, 0x9D0E, 0x4ce7, 0xAD, 0x39, 0xED, 0xD1, 0xAB, 0x83, 0x6B, 0x30],
       'AMITSE_BOOT_ORDER_CHANGE_GUID':[0x1b6bc809, 0xc986, 0x4937, 0x93, 0x4f, 0x1e, 0xa5, 0x86, 0x22, 0xfe, 0x50],
  21
       'AMITSE DRIVER HEALTH CTRL GUID':[0x58279c2d, 0xfb19, 0x466e, 0xb4, 0x2e, 0xcd, 0x43, 0x70, 0x16, 0xdc, 0x25],
  22
```





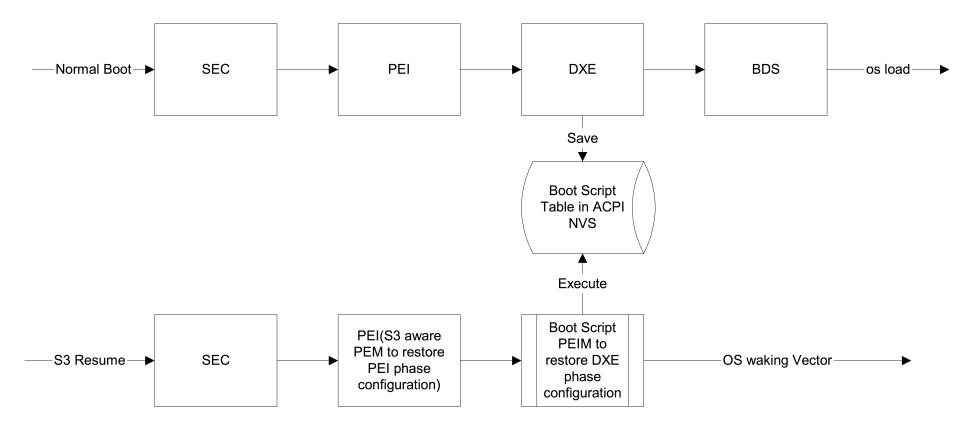
PI Boot Phases



EFI Boot Phases

- Different initialization phases.
- Make resources available to next phase.
- Memory for example.







The PEI/DXE Dispatchers

- PEI and DXE phases have a dispatcher.
- Guarantees dependencies and load order.
- Dependency expressions.
- Available as a section.



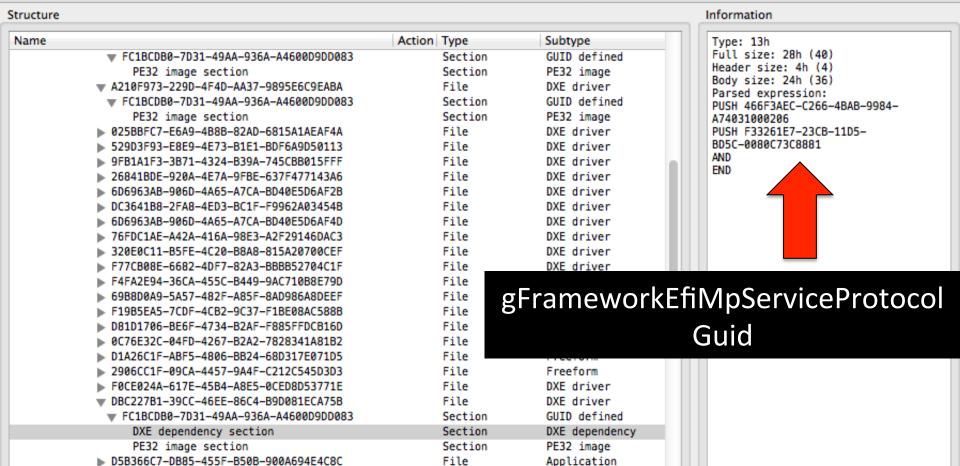
Information Structure Name Action Type Subtype Type: 1Bh Full size: 28h (40) 7A9354D9-0468-444A-81CE-0BF617D890DF Volume FFSv2 Header size: 4h (4) 52C05B14-0B98-496C-BC3B-04B50211D680 File PEI core Body size: 24h (36) TCA23D91-9C13-4679-A2B7-9DCEE98734A2 File PEI module Parsed expression: 38317FC0-2795-4DE6-B207-680CA768CFB1 File PEI module PUSH 6C83C560-C13F-450A-9993-PEI dependency section Section PEI dependency F1DFDD2C3286 TE image section Section TE image PUSH CCEE425A-63DE-45AB-BA0F-E9D7AFC5DAC8 34C8C28F-B61C-45A2-8F2E-89E46BECC63B File PEI module AND PEI dependency section Section PEI dependency END TE image section Section TE image 80F1DE13-3C6E-4A78-A802-1AC5FF3750FB File PEI module BAC57518-8934-423D-BB39-F5FC88840CCF File PEI module 6A09B044-D0D8-5AA8-A301-53FA273E2FD6 File PEI module D072670B-DC2C-4768-8102-99B4A9EF5EDC File PEI module PEI dependency section Section PEI dependency TE image section Section TE image CD2B6EB3-EA11-4848-B687-AFE57D3D1C0F File PEI module 4A991D46-D51B-54AE-9C5E-8F4A1F221B3D File PEI module A66A4162-0221-456D-A519-05C4E302A864 File PEI module

UEFITool 0.20.6 - bios_dump.bin





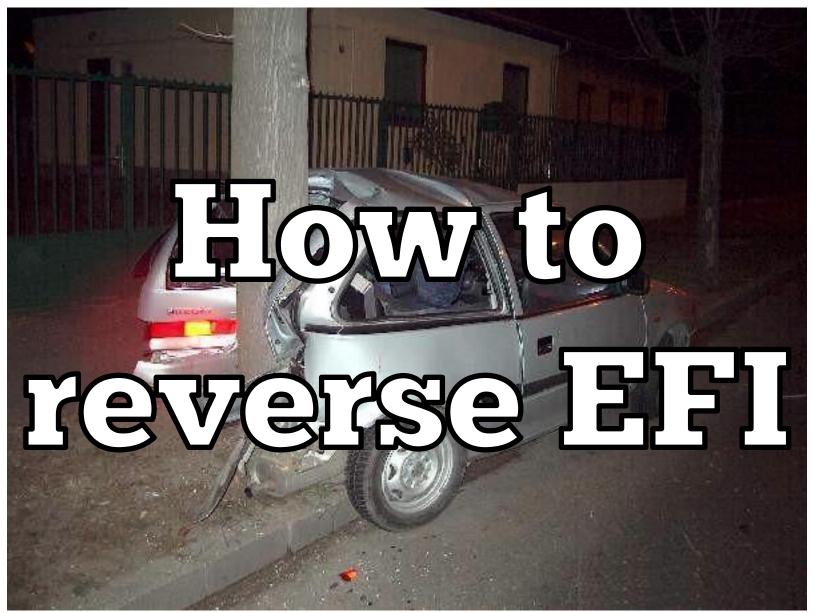
27247520 5020 4707 0222 152524750145



Eila

DVE deivor







Tools

- UEFITool and UEFIExtract
 - https://github.com/LongSoft/UEFITool
- Snare's IDA EFI Utils
 - https://github.com/snare/ida-efiutils/
- UEFI Firmware parser
 - https://github.com/snare/ida-efiutils/
- CHIPSEC
 - https://github.com/chipsec/chipsec



EFI file types

- Two executable file types.
- PE32/PE32+ (as in Windows).
- TE Terse Executable.
- 16/32/64 bit code, depending on phase.



TE file format

- TE is just a stripped version of PE.
- Unnecessary PE headers are removed.
- To save space.
- Used by SEC and PEI phase binaries.



TE file format

- IDA unable to correctly disassemble.
- Fails to parse the TE headers.
- Afaik, still not fixed in 6.8.
- Solution is to build your own TE loader.
- https://github.com/gdbinit/TELoader





- No standard libraries to link against.
- Instead there are services.
- Basic functions made available on each phase.
- Access via function pointers.



```
typedef struct _EFI_PEI_SERVICES {
  EFI TABLE HEADER
                                 Hdr;
  EFI PEI INSTALL PPI
                                 InstallPpi;
  EFI PEI REINSTALL PPI
                                 ReInstallPpi;
  EFI PEI LOCATE PPI
                                 LocatePpi;
  EFI PEI NOTIFY PPI
                                 NotifyPpi;
  EFI PEI GET BOOT MODE
                                 GetBootMode;
  EFI PEI SET BOOT MODE
                                 SetBootMode;
  EFI PEI GET HOB LIST
                                 GetHobList;
  EFI PEI CREATE HOB
                                 CreateHob;
  EFI PEI FFS FIND NEXT VOLUME
                                 FfsFindNextVolume;
  EFI PEI FFS FIND NEXT FILE
                                 FfsFindNextFile;
  EFI PEI FFS FIND SECTION DATA FfsFindSectionData;
  EFI PEI INSTALL PEI MEMORY
                                 InstallPeiMemory;
  EFI PEI ALLOCATE PAGES
                                 AllocatePages;
  EFI PEI ALLOCATE POOL
                                 AllocatePool;
  EFI PEI COPY MEM
                                 CopyMem;
  EFI PEI SET MEM
                                 CopyMem;
  EFI PEI REPORT STATUS_CODE
                                 CopyMem;
  EFI PEI RESET SYSTEM
                                 ResetSystem;
  EFI PEI CPU IO PPI
                                 CpuIo;
  EFI PEI PCI CFG PPI
                                 PciCfg;
} EFI PEI SERVICES;
```



```
typedef struct {
  EFI TABLE HEADER
                                  Hdr;
  EFI GET TIME
                                  GetTime;
  EFI SET TIME
                                  SetTime;
  EFI GET WAKEUP TIME
                                  GetWakeupTime;
  EFI SET WAKEUP TIME
                                  SetWakeupTime;
  EFI SET VIRTUAL ADDRESS MAP
                                  SetVirtualAddressMap;
  EFI CONVERT POINTER
                                  ConvertPointer;
  EFI GET VARIABLE
                                  GetVariable;
  EFI GET NEXT VARIABLE NAME
                                  GetNextVariableName;
  EFI SET VARIABLE
                                  SetVariable;
  EFI GET NEXT_HIGH_MONO_COUNT
                                  GetNextHighMonotonicCount;
  EFI RESET SYSTEM
                                  ResetSystem;
  EFI UPDATE CAPSULE
                                  UpdateCapsule;
  EFI QUERY CAPSULE CAPABILITIES QueryCapsuleCapabilities;
  EFI QUERY VARIABLE INFO
                                  QueryVariableInfo;
} EFI RUNTIME SERVICES;
```



- Each phase has different services.
- Entrypoint function contains a pointer to the tables.

```
typedef
EFI_STATUS
  (*EFI_IMAGE_ENTRY_POINT)(
   IN EFI_HANDLE ImageHandle,
   IN EFI_SYSTEM_TABLE *SystemTable <----- this one
);</pre>
```



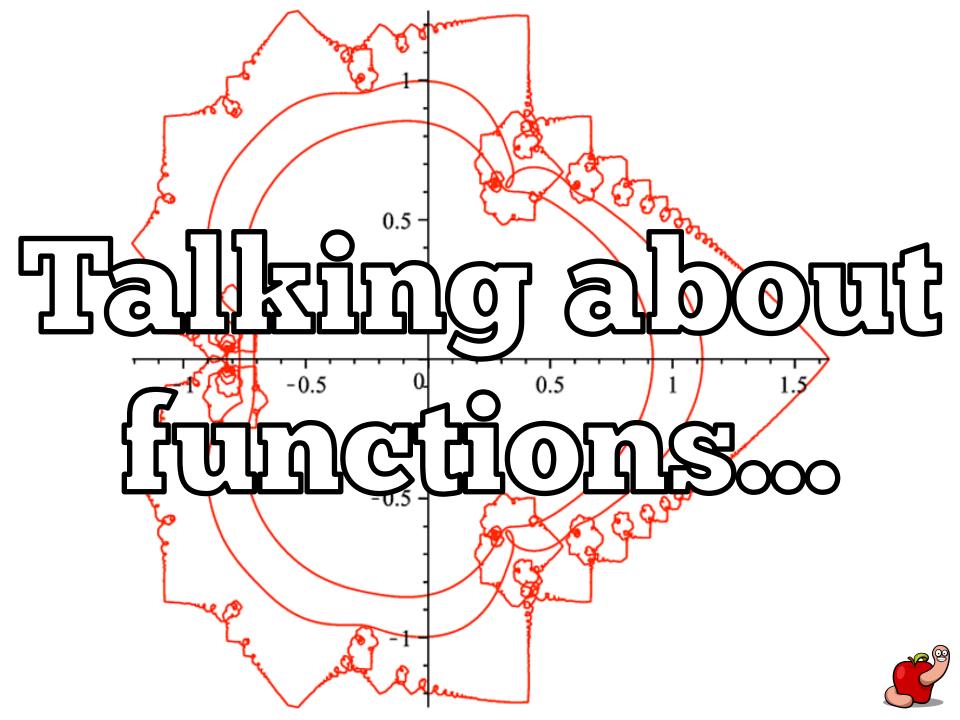
```
typedef struct {
  EFI TABLE HEADER Hdr;
  CHAR16 *FirmwareVendor;
  UINT32 FirmwareRevision;
  EFI HANDLE ConsoleInHandle;
  EFI SIMPLE TEXT INPUT PROTOCOL *ConIn;
  EFI HANDLE ConsoleOutHandle;
  EFI SIMPLE TEXT OUTPUT PROTOCOL *ConOut;
  EFI HANDLE StandardErrorHandle;
  EFI SIMPLE TEXT OUTPUT PROTOCOL *StdErr;
  EFI RUNTIME SERVICES *RuntimeServices; <- EFI RUNTIME SERVICES</pre>
  EFI BOOT SERVICES *BootServices; <- EFI BOOT SERVICES</pre>
  UINTN NumberOfTableEntries;
  EFI CONFIGURATION TABLE *ConfigurationTable;
} EFI SYSTEM TABLE;
```



Code that you often see in DXE drivers

```
.text:000000000000240 GetSystemTables proc near
                                                    ; CODE XREF: start+16
                                          cs:SystemTable, rdx
.text:000000000000240
                                  mov
                                          rax, [rdx+60h]
.text:0000000000000247
                                  mov
                                          cs:BootServices, rax
.text:00000000000024B
                                  mov
                                          rax, [rdx+58h]
.text:0000000000000252
                                  mov
                                          cs:RunTimeServices, rax
.text:0000000000000256
                                  mov
.text:000000000000025D
                                  xor
                                          eax, eax
.text:00000000000025F
                                  retn
.text:000000000000025F GetSystemTables endp
```





Calling conventions

- 32-bit binaries use standard C convention
 - Arguments passed on the stack.
 - SEC/PEI phase binaries.



```
call
        PeiPerfMeasure :
                              PEI PERF START (&PrivateData.PS,L"PreMem", NULL, mTick);
lea
        eax, [ebp+var C8]
        [esp+8], eax
mov
        eax, [ebp-268h]
lea
        [esp+4], eax
mov
        [esp], edi
mov
call
        PeiDispatcher; PeiDispatcher (PeiStartupDescriptor, &PrivateData, DispatchData);
cmp
        [ebp+var 9B], 1
        short loc FFEA736E
jz
        [esp], esi
mov
        dword ptr [esp+OCh], offset aPrivatedata pe ; "PrivateData.PeiMemoryInstalled == ((BOO"...
mov
        dword ptr [esp+8], 16Ch
mov
        dword ptr [esp+4], offset a EdkFoundati 4; "./Edk/Foundation/Core/Pei/PeiMain/PeiMa"...
mov
        PeiDebugAssert ; PEI ASSERT(&PrivateData.PS, PrivateData.PeiMemoryInstalled == TRUE);
call
```



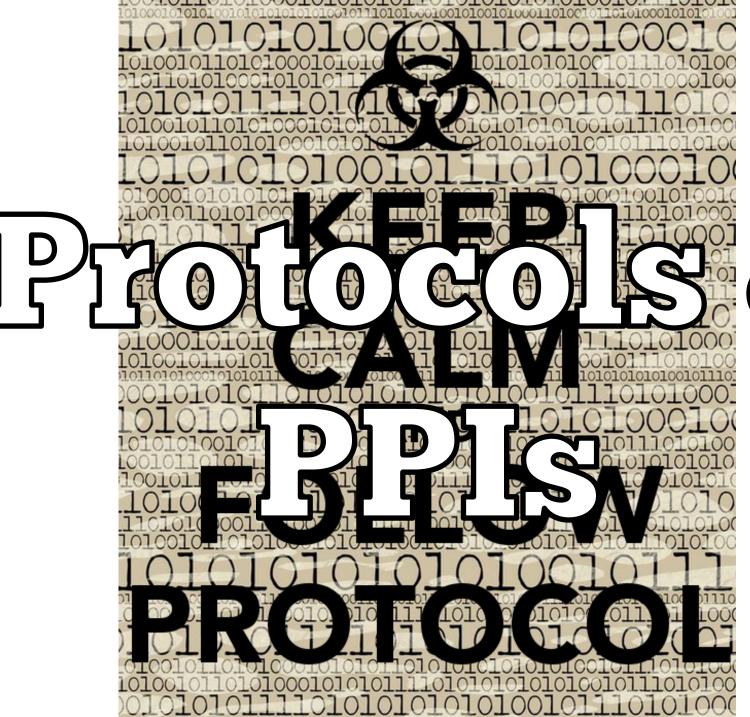
Calling conventions

- 64-bit binaries use Microsoft's x64
 - First four arguments: RCX, RDX, R8, R9.
 - Remaining on the stack.
 - 32-byte shadow space on stack.
 - First stack argument starts at offset 0x20.
 - DXE phase binaries.



```
rax, cs:1F688h
mov
        dword ptr [rsp+28h], 2 <- 6th
mov
        qword ptr [rsp+20h], 0 <- 5th
mov
        rdx, qword 1D7A0
lea
                             <- 2nd
        r8, [rbp+var 38]
lea
                                <- 3rd
        rcx, rdi
                                <- 1st
mov
        r9d, r9d
                                <- 4th
xor
call
        qword ptr [rax+118h]
```









Protocols & PPIs

- The basic services aren't enough.
- How are more services made available?
- Via Protocols and PPIs.
- Installed (published) by (U)EFI binaries.
- Others can locate and use them.



Protocols & PPIs

- Protocol (and PPI) is a data structure.
- Contains an identification, GUID.
- Optionally, function pointers and data.



```
| Protocol |
#define EFI ACPI S3_SAVE_GUID { 0x125f2de1, 0xfb85, 0x440c, 0xa5, 0x4c,
                                 0x4d, 0x99, 0x35, 0x8a, 0x8d, 0x38 }
typedef struct EFI ACPI S3 SAVE PROTOCOL {
 EFI_ACPI_GET_LEGACY_MEMORY_SIZE GetLegacyMemorySize;
 EFI ACPI S3 SAVE S3Save;
} EFI ACPI S3 SAVE PROTOCOL;
| Function Pointers|
typedef
EFI STATUS
(EFIAPI *EFI ACPI S3 SAVE)(
  IN EFI ACPI S3 SAVE PROTOCOL
                                     * This,
  IN VOID
                                     * LegacyMemoryAddress
  );
typedef
EFI STATUS
(EFIAPI *EFI ACPI GET LEGACY MEMORY SIZE)(
  IN EFI ACPI S3 SAVE PROTOCOL
                                    * This,
                                     * Size
  OUT UINTN
```

Protocols & PPIs

- Protocols exist in DXE phase.
- PPIs exist in PEI phase.
- In practice we can assume they are equivalent.



Sample PPI usage

First, locate the PPI.



Sample PPI usage

Second, use it.

```
if (Status == EFI_SUCCESS) {
   if (Capsule->CheckCapsuleUpdate ((EFI_PEI_SERVICES**)PeiServices) == EFI_SUCCESS) {
     BootMode = BOOT_ON_FLASH_UPDATE;
     Status = (*PeiServices)->SetBootMode((const EFI_PEI_SERVICES **)PeiServices, BootMode);
     ASSERT_EFI_ERROR (Status);
   }
}
```



Sample Protocol usage

```
#define EFI BOOT SCRIPT SAVE GUID \
{ 0x470e1529, 0xb79e, 0x4e32, 0xa0, 0xfe, 0x6a,0x15, 0x6d, 0x29, 0xf9, 0xb2 }
typedef struct EFI BOOT SCRIPT SAVE PROTOCOL {
    EFI BOOT SCRIPT WRITE Write;
    EFI BOOT SCRIPT CLOSE TABLE CloseTable;
} EFI BOOT SCRIPT SAVE PROTOCOL;
.data:000000000009D20 ; EFI GUID gEfiBootScriptSaveProtocolGuid
.data:000000000009D20 gEfiBootScriptSaveProtocolGuid dd 470E1529h
.data:0000000000009D20
                                     dw OB79Eh
.data:0000000000009D20
                                     dw 4E32h
                                     db OAOh, OFEh, 6Ah, 15h, 6Dh, 29h, OF9h, OB2h
.data:0000000000009D20
```



```
locate_bootscript_save_protocol proc near; CODE XREF: sub 180C+21
       push
              rbp
              rbp, rsp
       mov
              rsp, 20h
       sub
              rax, [rdx+60h] <- BootServices</pre>
      mov
              rcx, gEfiBootScriptSaveProtocolGuid <- GUID to locate
      lea
              r8, Boot Script Save Interface <- store pointer to table
      lea
              edx, edx
      xor
       call
              qword ptr [rax+140h] <- BootServices->LocateProtocol()
      test
              rax, rax
              short loc 281
      jns
       mov
              rcx, 800000000000014h
       cmp
              rax, rcx
      jz
            short loc 281
              cs:Boot Script Save Interface, 0
       mov
loc 281:
               ; CODE XREF: locate bootscript save protocol+25
                ; locate bootscript save protocol+34
              eax, eax
       xor
       add
              rsp, 20h
              rbp
       pop
       retn
locate bootscript save protocol endp
```

```
save script dispatch opcode proc near ; CODE XREF: sub 2D0F+6C
                                        ; sub_3C1A+83 ...
                        rbp
                 push
                        rbp, rsp
                 mov
                 sub
                        rsp, 20h
                        r9, rdx <- EntryPoint
                 mov
                        rdx, 800000000000000Eh
                 mov
                        rax, cs:Boot_Script_Save_Interface
                 mov
                 test rax, rax <- NULL ptr?
                        short loc 3E1
                 jz
                        edx, cx <- TableName
                movzx
                        rcx, rax <- *This
                 mov
                        r8d, 8 <- OpCode
                 mov
                        qword ptr [rax] <- BootScriptSave->Write()
                call
                        edx, edx
                 xor
loc 3E1:
                                        ; CODE XREF: save script dispatch opcode+1F
                        rax, rdx
                 mov
                        rsp, 20h
                 add
                        rbp
                 pop
                 retn
save_script_dispatch_opcode endp
```



Apple HH eustomizations



Apple Computer Inc.

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Apple EFI customizations

- Apple specific modifications.
- To reserved fields.
- Must be taken care of.
- Else bricked firmware.
- UEFITool vo.27+ handles everything.



EFI_FIRMWARE_VOLUME_HEADER

Summary

Describes the features and layout of the firmware volume.

Prototype

```
typedef struct {
  UINT8
                           ZeroVector[16];
  EFI GUID
                           FileSystemGuid;
 UINT64
                           FvLength;
 UINT32
                           Signature;
  EFI FVB ATTRIBUTES 2
                           Attributes;
  UINT16
                           HeaderLength;
  UINT16
                           Checksum;
  UINT16
                           ExtHeaderOffset:
 UINT8
                           Reserved[1];
                           Revision;
 UINT8
  EFI FV BLOCK MAP
                           BlockMap[];
} EFI FIRMWARE VOLUME HEADER;
```

Parameters

ZeroVector

The first 16 bytes are reserved to allow for the reset vector of processors whose reset vector is at address 0.



Apple EFI customizations

- The first 8 bytes.
- Constant between firmware volumes with the same GUID.
- Changes between versions?
- Unknown meaning, doesn't seem relevant.



Apple EFI customizations

- Next 4 bytes.
- CRC32 value.
- Of the firmware volume contents.
- By spec, header got its own 16-bit checksum.



100

Structure Information Action Type Name Subtype ZeroVector: 70 3D 75 55 00 00 00 00 ▼ Intel image Image Intel 3D 50 65 C8 D0 B1 06 00 Descriptor region Region Descriptor ritesystem Guiu: ME region Region ME 7A9354D9-0468-444A-81CE-0BF617D890D ■ BIOS region Region BIOS FFSv2 7A9354D9-0468-444A-81CE-0BF617D890DF Volume Full size: A0000h (655360) Volume 7A9354D9-0468-444A-81CE-0BF617D890DF FFSv2 Header size: 48h (72) Body size: 9FFB8h (655288) 7A9354D9-0468-444A-81CE-0BF617D890DF Volume FFSv2 Revision: 1 Volume Unknown E3B980A9-5FE3-48E5-9B92-2798385A9027 Attributes: FFFF8E7Fh Volume FFSv2 7A9354D9-0468-444A-81CE-0BF617D890DF Erase polarity: 1 Volume FFSv2 7A9354D9-0468-444A-81CE-0BF617D890DF Volume Unknown 153D2197-29BD-44DC-AC59-887F70E41A6B Unknown 153D2197-29BD-44DC-AC59-887F70E41A6B Volume Volume FFF12B8D-7696-4C8B-A985-2747075B4F50 Unknown Volume FFSv2 7A9354D9-0468-444A-81CE-0BF617D890DF Volume FFSv2 7A9354D9-0468-444A-81CE-0BF617D890DF File PEI core 52C05B14-0B98-496C-BC3B-04B50211D680 File PEI module 80F1DE13-3C6E-4A78-A802-1AC5FF3750FB File PEI module 38317FC0-2795-4DE6-B207-680CA768CFB1 File PEI module 34C8C28F-B61C-45A2-8F2E-89E46BECC63B File PEI module 8A78B107-0FDD-4CC8-B7BA-DC3E13CB8524 27A5159D-5E61-4809-919A-422E887101EF File PEI module Ø1359D99-9446-456D-ADA4-50A711C03ADA File PEI module File PEI module EDF59D2E-D5D6-4A63-A298-8FF2FA47D20B File PEI module 53984C6A-1B4A-4174-9512-A65E5BC8B278 996D8FF2-703F-492C-9A50-1DBEB32AAEB1 File PEI module 320A5BFC-E508-4D92-9255-BBB10AEF6A30 File PEI module 01187BBB-DD3E-4D06-BA29-F09B92496599 File PEI module File C779F6D8-7113-4AA1-9648-EB1633C7D53B PEI module

File

Cila

PEI module

DET modulo

Messages

parseVolume: unknown file system E3B980A9-5FE3-48E5-9B92-2798385A9027
parseVolume: unknown file system 153D2197-29BD-44DC-AC59-887F70E41A6B
parseVolume: unknown file system 153D2197-29BD-44DC-AC59-887F70E41A6B
parseVolume: unknown file system FFF12B8D-7696-4C8B-A985-2747075B4F50

Opened: Retina-30-07-2015-after-Secuinside-2015.bin

233DF097-3218-47B2-9E09-FE58C2B20D22

ACCAMICA MARI MECH ACID MECACAMANOCA

Structure Information Action Type Name Subtype ZeroVector: **----**0 00 00 00 ▼ Intel image Image Intel 3D 50 65 C8 🛑 B1 06 00 Descriptor region Region Descriptor ME region Region ME 7A9354D9-0468-444A-81CE-0BF617D890D ■ BIOS region Region BIOS FFSv2 7A9354D9-0468-444A-81CE-0BF617D890DF Volume Full size: A0000h (655360) Volume 7A9354D9-0468-444A-81CE-0BF617D890DF FFSv2 Header size: 48h (72) Body size: 9FFB8h (655288) 7A9354D9-0468-444A-81CE-0BF617D890DF Volume FFSv2 Revision: 1 Volume Unknown E3B980A9-5FE3-48E5-9B92-2798385A9027 Attributes: FFFF8E7Fh Volume FFSv2 7A9354D9-0468-444A-81CE-0BF617D890DF Erase polarity: 1 Volume FFSv2 7A9354D9-0468-444A-81CE-0BF617D890DF Volume Unknown 153D2197-29BD-44DC-AC59-887F70E41A6B Unknown 153D2197-29BD-44DC-AC59-887F70E41A6B Volume Volume FFF12B8D-7696-4C8B-A985-2747075B4F50 Unknown Volume FFSv2 7A9354D9-0468-444A-81CE-0BF617D890DF Volume FFSv2 7A9354D9-0468-444A-81CE-0BF617D890DF File PEI core 52C05B14-0B98-496C-BC3B-04B50211D680 File PEI module 80F1DE13-3C6E-4A78-A802-1AC5FF3750FB File PEI module 38317FC0-2795-4DE6-B207-680CA768CFB1 File PEI module 34C8C28F-B61C-45A2-8F2E-89E46BECC63B File PEI module 8A78B107-0FDD-4CC8-B7BA-DC3E13CB8524 27A5159D-5E61-4809-919A-422E887101EF File PEI module Ø1359D99-9446-456D-ADA4-50A711C03ADA File PEI module File PEI module EDF59D2E-D5D6-4A63-A298-8FF2FA47D20B File PEI module 53984C6A-1B4A-4174-9512-A65E5BC8B278 996D8FF2-703F-492C-9A50-1DBEB32AAEB1 File PEI module 320A5BFC-E508-4D92-9255-BBB10AEF6A30 File PEI module 01187BBB-DD3E-4D06-BA29-F09B92496599 File PEI module File C779F6D8-7113-4AA1-9648-EB1633C7D53B PEI module

File

Cila

PEI module

DET modulo

Messages

parseVolume: unknown file system E3B980A9-5FE3-48E5-9B92-2798385A9027 parseVolume: unknown file system 153D2197-29BD-44DC-AC59-887F70E41A6B parseVolume: unknown file system 153D2197-29BD-44DC-AC59-887F70E41A6B parseVolume: unknown file system FFF12B8D-7696-4C8B-A985-2747075B4F50

233DF097-3218-47B2-9E09-FE58C2B20D22

ACCAMICA MARI MECH ACID MECACAMANOCA

Apple EFI customizations

- Last 4 bytes.
- Total space used by firmware files.
- Must be updated if there are any modifications to volume free space.
- Bricked firmware if wrong.



Structure Information Action Type Name Subtype ZeroVector: 70 3D 75 5 ▼ Intel image Image Intel 3D 50 65 (D0 B1 06 00 Descriptor region Region Descriptor FileSystem ME region Region ME 7A9354D9-0468-444A-81CE-0BF617D890D ■ BIOS region Region BIOS FFSv2 Full size: A0000h (655360) 7A9354D9-0468-444A-81CE-0BF617D890DF Volume 7A9354D9-0468-444A-81CE-0BF617D890DF Volume FFSv2 Body size: 9FFB8h (655288) Volume FFSv2 7A9354D9-0468-444A-81CE-0BF617D890DF Revision: 1 Volume Unknown E3B980A9-5FE3-48E5-9B92-2798385A9027 Attributes: FFFF8E7Fh 7A9354D9-0468-444A-81CE-0BF617D890DF Volume FFSv2 Erase polarity: 1 Volume FFSv2 7A9354D9-0468-444A-81CE-0BF617D890DF Volume Unknown 153D2197-29BD-44DC-AC59-887F70E41A6B Unknown 153D2197-29BD-44DC-AC59-887F70E41A6B Volume Volume FFF12B8D-7696-4C8B-A985-2747075B4F50 Unknown Volume FFSv2 7A9354D9-0468-444A-81CE-0BF617D890DF Volume FFSv2 7A9354D9-0468-444A-81CE-0BF617D890DF File PEI core 52C05B14-0B98-496C-BC3B-04B50211D680 File PEI module 80F1DE13-3C6E-4A78-A802-1AC5FF3750FB File PEI module 38317FC0-2795-4DE6-B207-680CA768CFB1 File PEI module 34C8C28F-B61C-45A2-8F2E-89E46BECC63B File PEI module 8A78B107-0FDD-4CC8-B7BA-DC3E13CB8524 27A5159D-5E61-4809-919A-422E887101EF File PEI module Ø1359D99-9446-456D-ADA4-50A711C03ADA File PEI module File PEI module EDF59D2E-D5D6-4A63-A298-8FF2FA47D20B File PEI module 53984C6A-1B4A-4174-9512-A65E5BC8B278 996D8FF2-703F-492C-9A50-1DBEB32AAEB1 File PEI module 320A5BFC-E508-4D92-9255-BBB10AEF6A30 File PEI module 01187BBB-DD3E-4D06-BA29-F09B92496599 File PEI module File C779F6D8-7113-4AA1-9648-EB1633C7D53B PEI module

File

Cila

PEI module

DET modulo

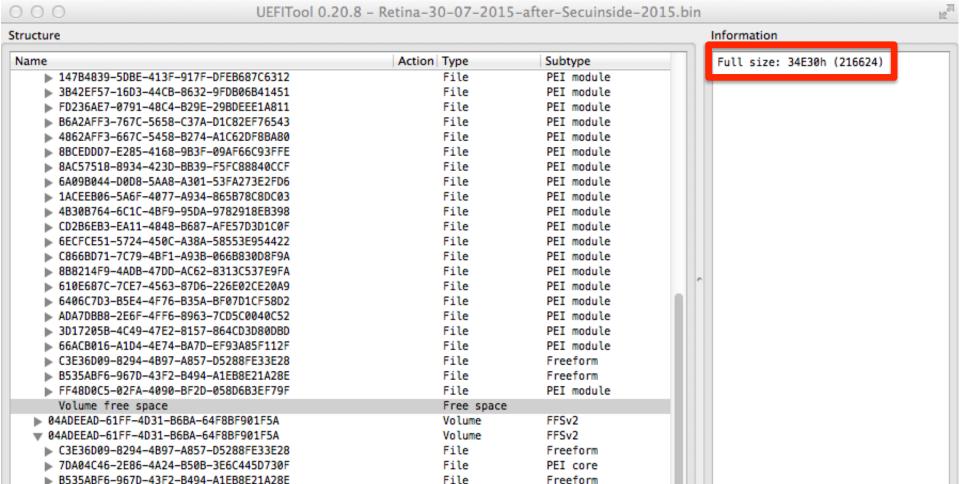
Messages

parseVolume: unknown file system E3B980A9-5FE3-48E5-9B92-2798385A9027 parseVolume: unknown file system 153D2197-29BD-44DC-AC59-887F70E41A6B parseVolume: unknown file system 153D2197-29BD-44DC-AC59-887F70E41A6B parseVolume: unknown file system FFF12B8D-7696-4C8B-A985-2747075B4F50

Opened: Retina-30-07-2015-after-Secuinside-2015.bin

233DF097-3218-47B2-9E09-FE58C2B20D22

ACCAMICA MARI MECH ACID MECACAMANOCA



File

Cila

Pad

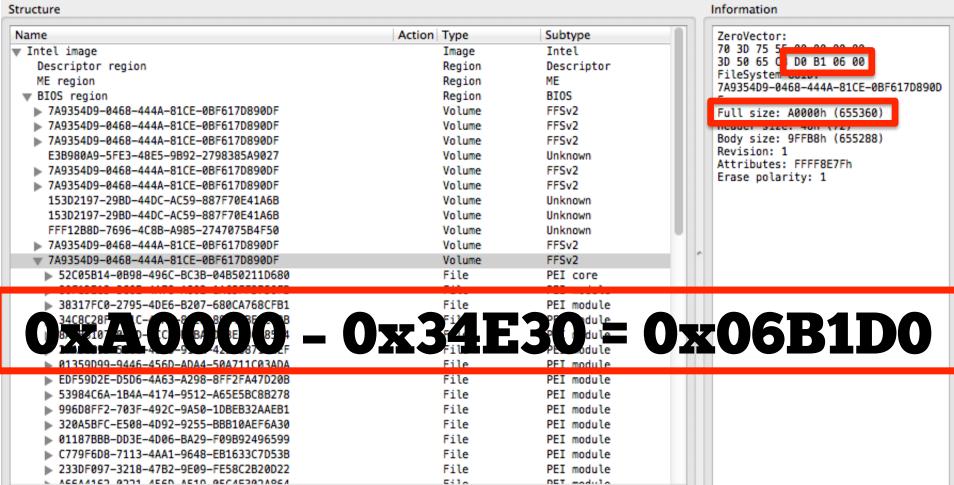
CEC core

Messages

Pad-file

parseVolume: unknown file system E3B980A9-5FE3-48E5-9B92-2798385A9027
parseVolume: unknown file system 153D2197-29BD-44DC-AC59-887F70E41A6B
parseVolume: unknown file system 153D2197-29BD-44DC-AC59-887F70E41A6B
parseVolume: unknown file system FFF12B8D-7696-4C8B-A985-2747075B4F50

► 1DAGGETE CTTO AEOT OFFE TREATOFTOEGO



Messages

parseVolume: unknown file system E3B980A9-5FE3-48E5-9B92-2798385A9027 parseVolume: unknown file system 153D2197-29BD-44DC-AC59-887F70E41A6B parseVolume: unknown file system 153D2197-29BD-44DC-AC59-887F70E41A6B parseVolume: unknown file system FFF12B8D-7696-4C8B-A985-2747075B4F50

Opened: Retina-30-07-2015-after-Secuinside-2015.bin



- Dump the flash contents.
 - Via hardware, if possible.
- Have a known good image.
 - A previously certified/trusted dump.
 - Or firmware updates.



- Firmware updates available from Apple.
- Direct downloads.
 - https://support.apple.com/en-us/HT201518
- Or combined with OS installer or updates.
- No hashes from Apple available (yet).



- Only useful for machines with available updates.
- Newly released machines need to wait for a firmware update.



- Firmware & signatures vault
 - https://github.com/gdbinit/firmware_vault
- Signed by my PGP key.
- Extracted from available Apple updates.
- Soon, the SMC updates.

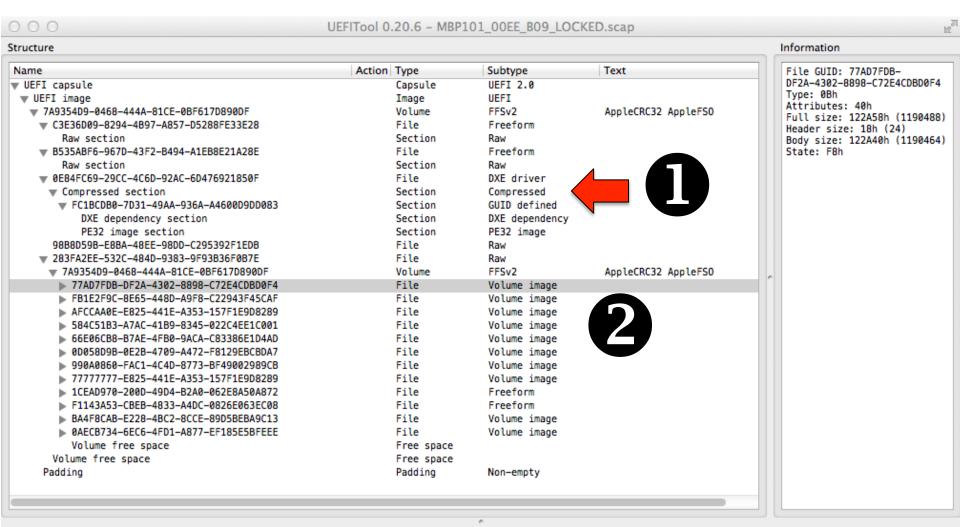


- Two file formats used for updates.
- SCAP (most common).
- FD (some newer and older models).
- UEFITool can process both.



- EFI Capsule.
- Used to deliver updates.
- Recommended delivery mechanism.
- Composed by firmware volumes.
- Flash dumps parser can be reused.





Messages

parseVolume: unknown file system E3B980A9-5FE3-48E5-9B92-2798385A9027 parseVolume: unknown file system FFF12B8D-7696-4C8B-A985-2747075B4F50 parseVolume: unknown file system 153D2197-29BD-44DC-AC59-887F70E41A6B

- are the BIOS region contents.
- Encapsulated on different GUIDs.



■ 8E84FC69-29CC-4C6D-92AC-6D476921850F File DXE driver					
9888D59B-E8BA-48EE-98D0-C295392F1EDB	Name	Action	Type	Subtype	Text
▼ 283FA2EE-532C-484D-9383-9F93B36F087E	▶ 0E84FC69-29CC-4C6D-92AC-6D476921850F		File	DXE driver	
▼ 7A9354D9-8468-444A-81CE-8BF617D890DF	98B8D59B-E8BA-48EE-98DD-C295392F1EDB		File	Raw	
77AD7FDB-DF2A-4302-8898-C72E4CDBD0F4	▼ 283FA2EE-532C-484D-9383-9F93B36F0B7E		File	Raw	
FB1E2F9C-8E65-448D-A9F8-C22943F45CAF	▼ 7A9354D9-0468-444A-81CE-0BF617D890DF		Volume	FFSv2	AppleCRC32 AppleFS0
AFCCAA0E_E825_441E_A353_157F1E9D8289	77AD7FDB-DF2A-4302-8898-C72E4CDBD0F4		File	Volume image	
584C5183-A7AC-4189-8345-022C4EE1C001	FB1E2F9C-8E65-448D-A9F8-C22943F45CAF		File	Volume image	
■ 66E06CB8-B7AE-4FB0-9ACA-C83386E1D4AD	▶ AFCCAA0E-E825-441E-A353-157F1E9D8289		File	Volume image	
▼ 0D058D9B-0E2B-4709-A472-F8129EBCBDA7 File Volume image ▼ Compressed section Section Compressed ▼ FC1BCD8P-7D31-49AA-936A-A4600D9DD083 Section GUID defined ▼ Volume image section Section Volume image FFF12B8D-7696-4C8B-A985-2747075B4F50 Volume Unknown ▼ 990A0860-FAC1-4C4D-8773-BF49002989CB File Volume image ▼ Compressed section Section GUID defined ▼ Volume image section Section Wolume image 153D2197-298D-44DC-AC59-887F70E41A6B Volume Unknown AppleCRC32 ▼ 77777777-E825-441E-A353-157F1E9D8289 File Volume image ▼ Compressed section Section Compressed ▼ Compressed section Section GUID defined ▼ Volume image section Section Wolume image ▼ Compressed section Section GUID defined ▼ Volume image section Volume image ▼ Compressed section Section GUID defined ▼ Volume image section Volume image Fise ▼ Compressed section Volume image	▶ 584C51B3-A7AC-41B9-8345-022C4EE1C001		File	Volume image	
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▼ FC1BCDB0-7D31-49AA-936A-A4600D9DD083	▼ 0D058D9B-0E2B-4709-A472-F8129EBCBDA7		File	Volume image	
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▼ 7777777-E825-441E-A353-157F1E9D8289 ▼ Compressed section ▼ FC1BCDB0-7D31-49AA-936A-A4600D9DD083 ▼ Volume image ▼ Volume image section ■ Volume image ■ 04ADEEAD-61FF-4D31-B6BA-64F8BF901F5A ■ 1CEAD970-200D-49D4-B2A0-062E8A50A872 ■ F1143A53-CBEB-4833-A4DC-0826E063EC08 ■ BA4F8CAB-E228-4BC2-8CCE-89D5BEBA9C13 ■ 0AECB734-6EC6-4FD1-A877-EF185E5BFEEE ▼ Volume free space ▼ Volume free space ▼ Free space ▼ Free space ▼ Free space	▼ Volume image section		Section	Volume image	
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Volume free space Volume free space Free space Free space				_	
Volume free space Free space			File	Volume image	
Padding Padding Non-empty					
	Padding		Padding	Non-empty	



- **1** is NVRAM region.
- 2 is Microcode.

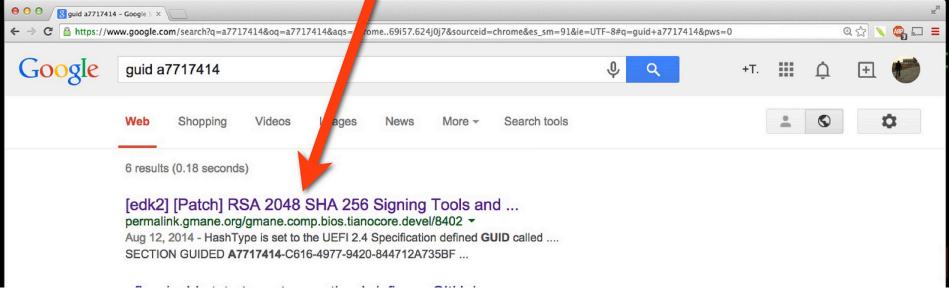
■ **3** is Boot volume.



- SCAP is signed.
- RSA2048 SHA256.
- Apple backported from UEFI.
- First reported by Trammell Hudson.



GUID a7717414-c616-4977-9420844712a735bf





- Compare the flash dump against SCAP.
- Locate all EFI binaries in the dump.
- Checksum against SCAP contents.



- We also need to verify:
 - New files.
 - Missing files.
 - Free/padding space?



- Verify NVRAM contents!
- Boot device is stored there.
- HackingTeam had a new variable there.
 - A simple "fuse" to decide to infect or not target system.



```
+....<T.i.m.e.o.u.t.....U.....&......g.
.....H..l.^.,.*...A.c.p.i.G.l.o.b.a.l.V.a.
r.i.a.b.l.e....P......U........a...
.........+..b..L.a.n.g...eng.U......
.....M.8jJ..K.....`...A.L.S._.D.a.t.a....
.........*.....8.%..... _.....&.Cu..]F.z.
p......P.\.S.y.s.t.e.m.\.L.i.b.r.a.r.y.\
.C.o.r.e.S.e.r.v.i.c.e.s.\.b.o.o.t...e.f.i
zB.o.o.t.O.r.d.e.r.....U.....@.....ac
I*..K...A.\.....b.l.u.e.t.o.o.t.h.I.n.t.e
.r.n.a.l.C.o.n.t.r.o.l.l.e.r.I.n.f.o.....
.....aCl*..K...A.\.
.....f.m.m.-.c.o.m.p.u.t.e.r.-.n.a.m.e...x
xx.U.....aCl*..K...A.\.....a.p.
L..h.hn0...D!g.p.u.-.p.o.w.e.r.-.p.r.e.f.s
.......U..........L./..L..h.hn0.y..
.a.p.u.-.a.c.t.i.v.e......U.....&.....
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</key><dict><key>IOProviderClass</key><str
ing>IOMedia</string><key>IOPropertyMatch</
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DECE-4A15-9EF2-DB878CF7A3E0</string></dict
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sk0s1</string></dict><dict><key>IOEFIDevic
ePathType</key><string>MediaFilePath</stri
ng><key>Path</key><string>\EFI\APPLE\FIRMW
ARE\MBP101_00EE_B07_LOCKED.scap</string></
dict></array>..U.....".....a.......
```



```
BOOLEAN
EFIAPI
CheckfTA()
   EFI STATUS
                              Status = EFI SUCCESS;
   UINTN
         VarDataSize;
   UINT8
         VarData;
   VarData=0;
   VarDataSize=sizeof(VarData);
   Status=gRT->GetVariable(L"fTA", &gEfiGlobalFileVariableGuid, NULL, &VarDataSize, (UINTN*)&VarData);
   if(Status!=EFI SUCCESS || VarData==0)
#ifdef FORCE DEBUG
                                   INFECT SYSTEM
       Print(L"Devo Infettare\n");
#endif
       return FALSE;
#ifdef FORCE DEBUG
                                 DO NOT INFECT SYSTEM
   Print(L"NON Devo Infettare\n");
#endif
   return TRUE;
```



- Don't forget boot.efi.
- Not very stealth.
- Always keep in mind that sophistication is not always required!
- If it works, why not?



How to find EFI monsters

- SCAP is used by EfiFlasher.
- We can stitch our own firmware.
- Extract files from SCAP and build it.
- Reflash via SPI.
- Assumption that SCAP is legit.



How to find EFI monsters

- Stitch utility still in TODO list.
- Potential issues:
 - NVRAM contents?
 - Serial numbers?
- Use current dump and just replace binaries?



BL BLA B B BLAH BLAH BLA



- (U)EFI rootkits aren't unicorns.
- Although they are very rare.
- Honestly, we don't know what's out there.
- HackingTeam developed one in 2014.
- Although it was too simple and not advanced.



- Chasing them requires hardware assistance.
- Disassembling computers monthly is not scalable/efficient/viable.
- How to deal with this at enterprise level?



- Vendors are usually slow releasing updates.
- If they ever do it.
- Check legbacore.com work.



- SMC is another interesting chip.
- Alex Ionescu and Andrea Barisani did some work in this area.
- Great rootkit possibilities?



- Intel Management Engine (ME).
- Big Pandora Box?
- Security researchers should have easier access to it.



- Option ROMs.
- Still an issue with Apple's EFI implementation.
- No SecureBoot (signed OptionROMs).
- Check Thunderstrike 2 OptionROM worm.







Footage released of Guardian editors destroying Snowden hard drives

GCHQ technicians watched as journalists took angle grinders and drills to computers after weeks of tense negotiations

Watch the footage of the hard drives being destroyed



New video footage has been released for the first time of the moment Guardian editors destroyed computers used to store top-secret documents leaked by the NSA whistleblower Edward Snowden.



Photo: John Stillwell/PA Wire/AP



Aug. 26 2015, 4:05 p.m.

The Way GCHQ Obliterated The Guardian's Laptops May Have Revealed More Than It Intended

In July 2013, GCHQ, Britain's equivalent of the U.S. National Security Agency, forced journalists at the London headquarters of *The Guardian* to completely obliterate the memory of the computers on which they kept copies of top-secret documents provided to them by former NSA contractor and whistle-blower Edward Snowden.



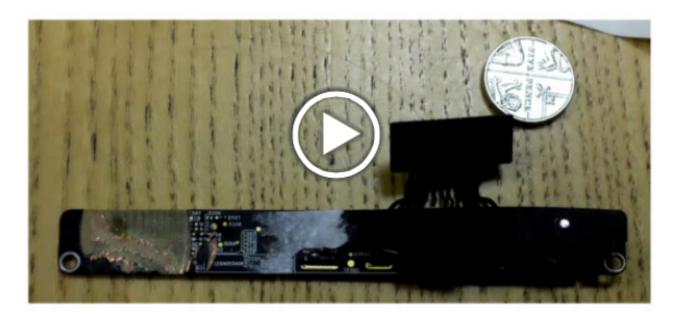
How to Destroy a Laptop with Top Secrets

How did GCHQ do it to the Guardian's copy of Snowden's files?

Mustafa Al-Bassam and Richard Tynan

Video Audio Download Share

Power Controller





- Trolling?
- Real?
- Maybe a mix of both.
- Check Apple logic board schematics.
- There's a ton of interconnected stuff.



- We need trusted hardware solutions.
- If we can't trust hardware we are wasting a lot of time solving some software problems.



- Bring back physical protections?
- Switches to enable:
 - Flash writes.
 - **■** MIC.
 - Camera.
 - Etc...



Jumper JP4: BIOS Flash Protect

The system BIOS and CMOS Setup Utility are stored in Flash memory on the motherboard, which provides permanent storage, but is rewritable, allowing for BIOS updates. Jumper JP4 controls the protection scheme that prevents accidental damage to or rewriting of the data stored in Flash memory.

JP4: BIOS Flash Protect

Setting	Function
Short 1-2 •• •	Protection mode selected in BIOS CMOS Setup Utility [Default]
Short 2-3 O • •	Protection enabled in hardware
Open [Remove Cap]	No BIOS Flash Protection





- Acer C720 & C720P Chromebook.
 - https://www.chromium.org/chromium-os/ developer-information-for-chrome-osdevices/acer-c720-chromebook
- #7 is a write-protect screw.



- Might require new hardware design?
- NVRAM needs to be writable.
- An independent flash chip for writable regions?
- BOM/space restrictions?



- Apple has a great opportunity here.
- Full control of design and supply chain.
- Can improve designs.
- Can force faster updates.
- Only matched by Chromebook?

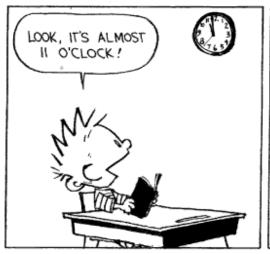


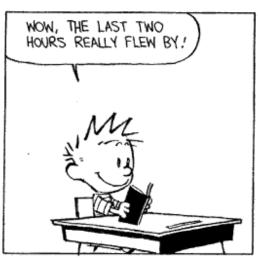
23) 出(公司

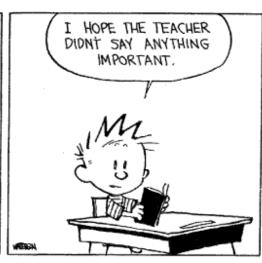


Greetings

CODE BLUE team, Snare, Trammell, Xeno,
 Corey, Saure, cr4sh.









https://reverse.put.as https://github.com/gdbinit reverser@put.as @osxreverser

#osxre @ irc.freenode.net

PGP key

https://reverse.put.as/wp-content/uploads/2008/06/publickey.txt

PGP Fingerprint

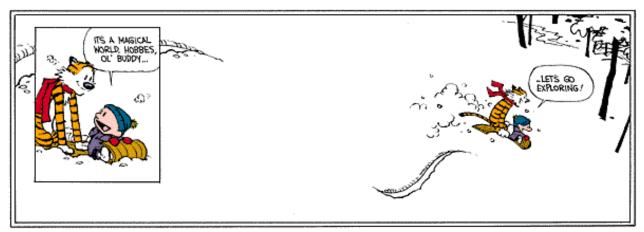
7B05 44D1 A1D5 3078 7F4C E745 9BB7 2A44 ED41 BF05



A day full of possibilities!







Let's go exploring!



- Images from images.google.com. Credit due to all their authors.
- Thunderstrike presentation
 - https://trmm.net/Thunderstrike_31c3
- Thunderstrike 2 presentation
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